

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION SERIAL NO 09/921,518
 FILING DATE August 1, 2001
 INVENTORSHIP John T. Moore
 GROUP ART UNIT 2818
 EXAMINER Phuc T. Dang
 ATTORNEY'S DOCKET NO MI22-1669
 TITLE: Method of Forming Integrated Circuitry, Method of Forming Memory
 Circuitry, and Method of Forming Non-Volatile Random Access
 Memory Circuitry

Assistant Commissioner for Patents
ATTENTION: Official Draftsman
 Washington, D.C. 20231

LETTER SUBMITTING FORMAL DRAWINGS

Please enter the enclosed formal drawing sheets (Sheets 1/5 and 2/5) in the above-referenced application in place of the originally filed drawing sheets. These substitute formal sheets are being submitted to correct a numbering error of a typographical nature in Figs. 1 and 2. The proposed changes are indicated in red on attached copies. The substitute formal drawings incorporate the proposed changes.

Acknowledgment of receipt of the substitute formal drawing sheets and their acceptance into the file is requested.

Respectfully submitted,

Date: 4-30-02

By: 

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Enclosures: 2 sheets of red-lined drawings (Sheets 1/5 and 2/5 - Figs. 1 and 2);
 2 sheets of substitute formal drawings (Sheets 1/5 and 2/5 - Figs. 1 and 2)

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